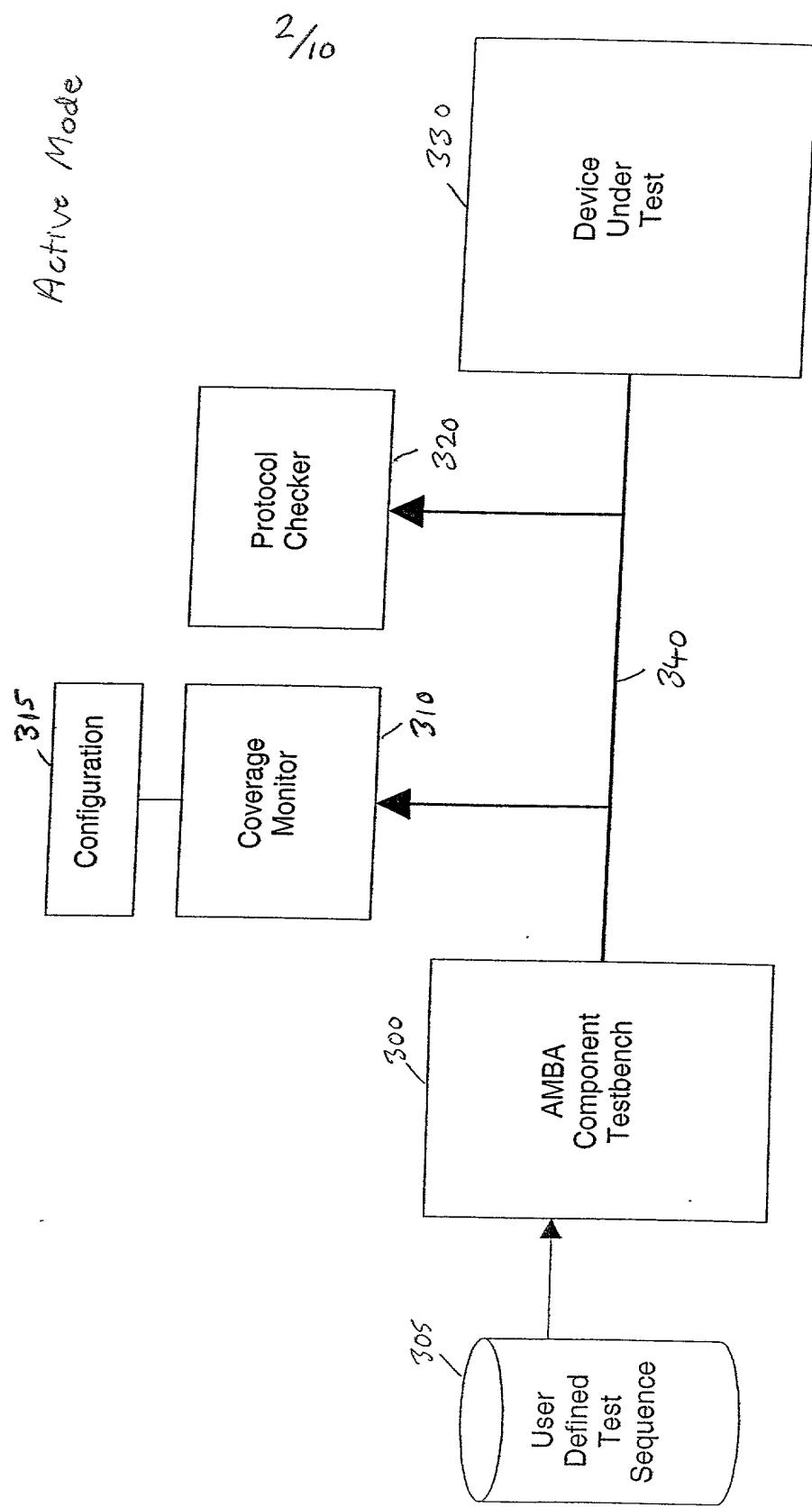
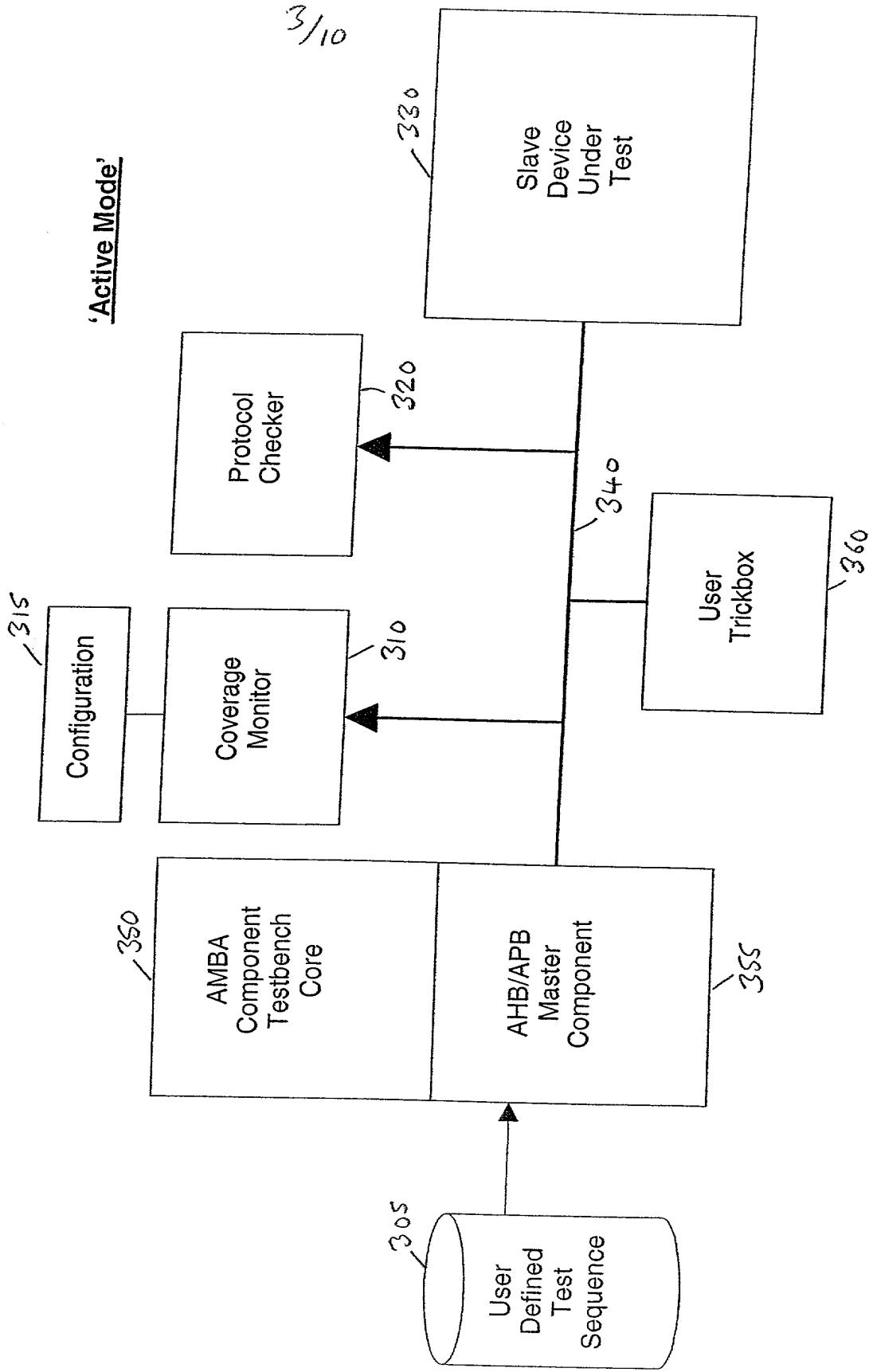


FIG. 1





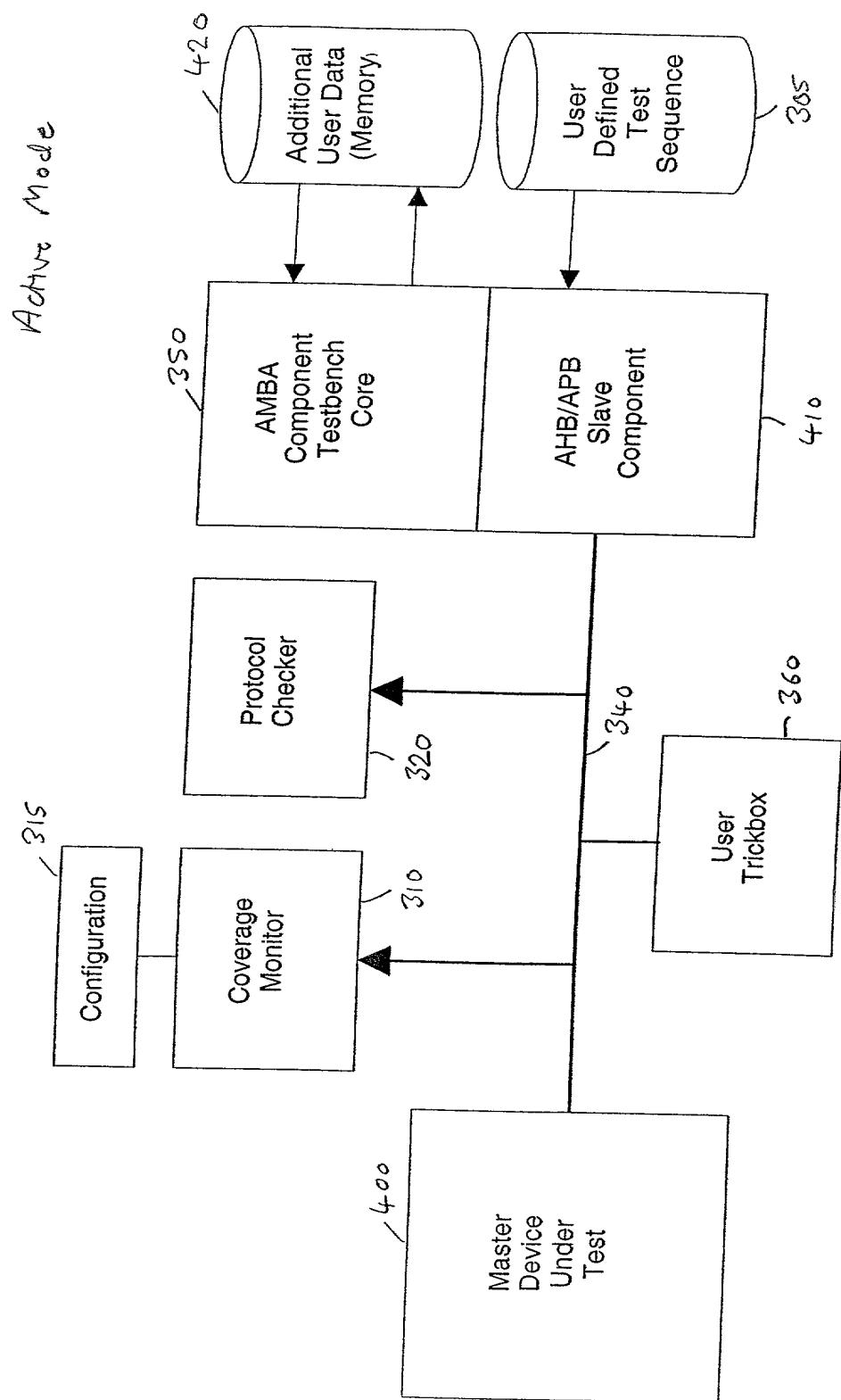


FIG. 4

'Passive Mode'

System Simulation Environment

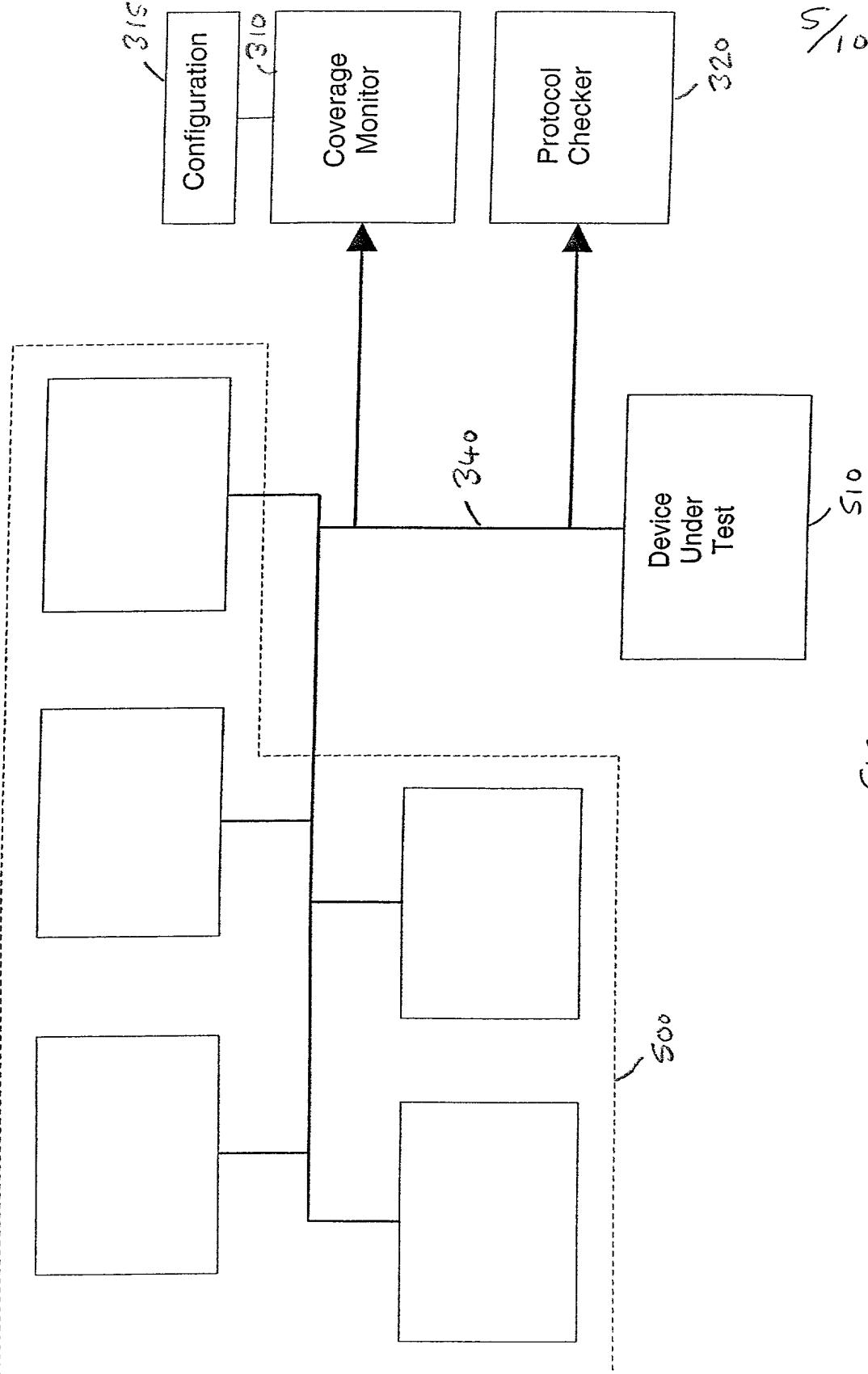


FIG. 5

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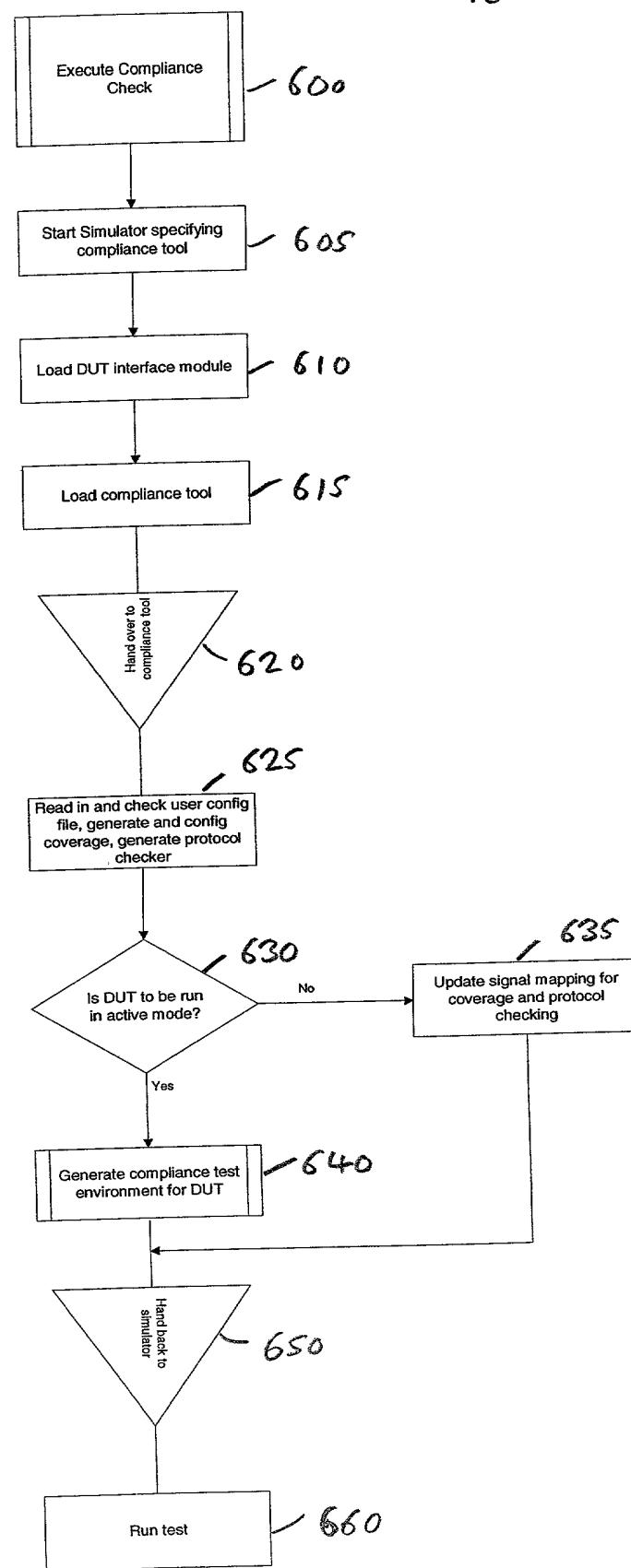


FIG. 6

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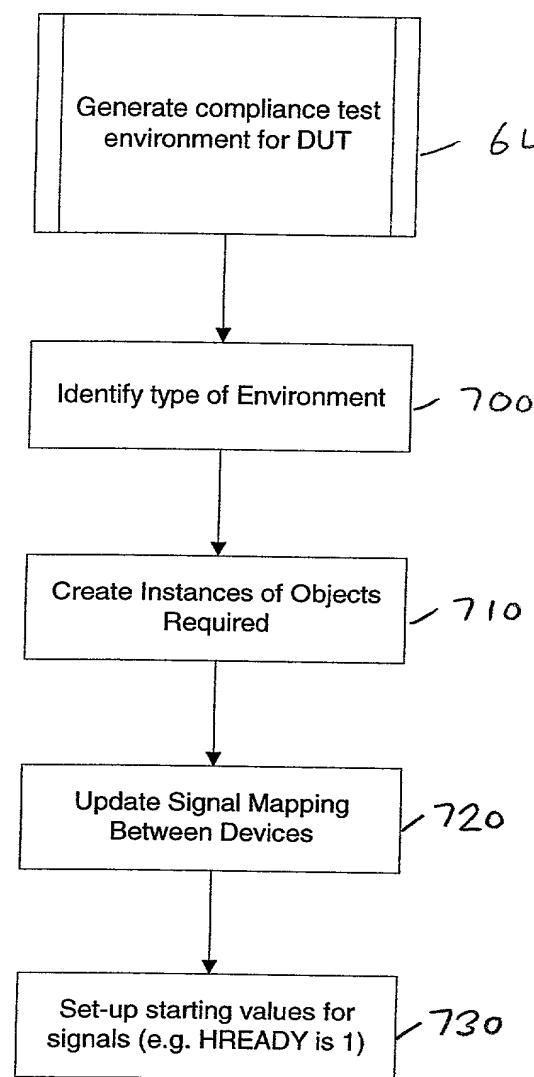


FIG. 7

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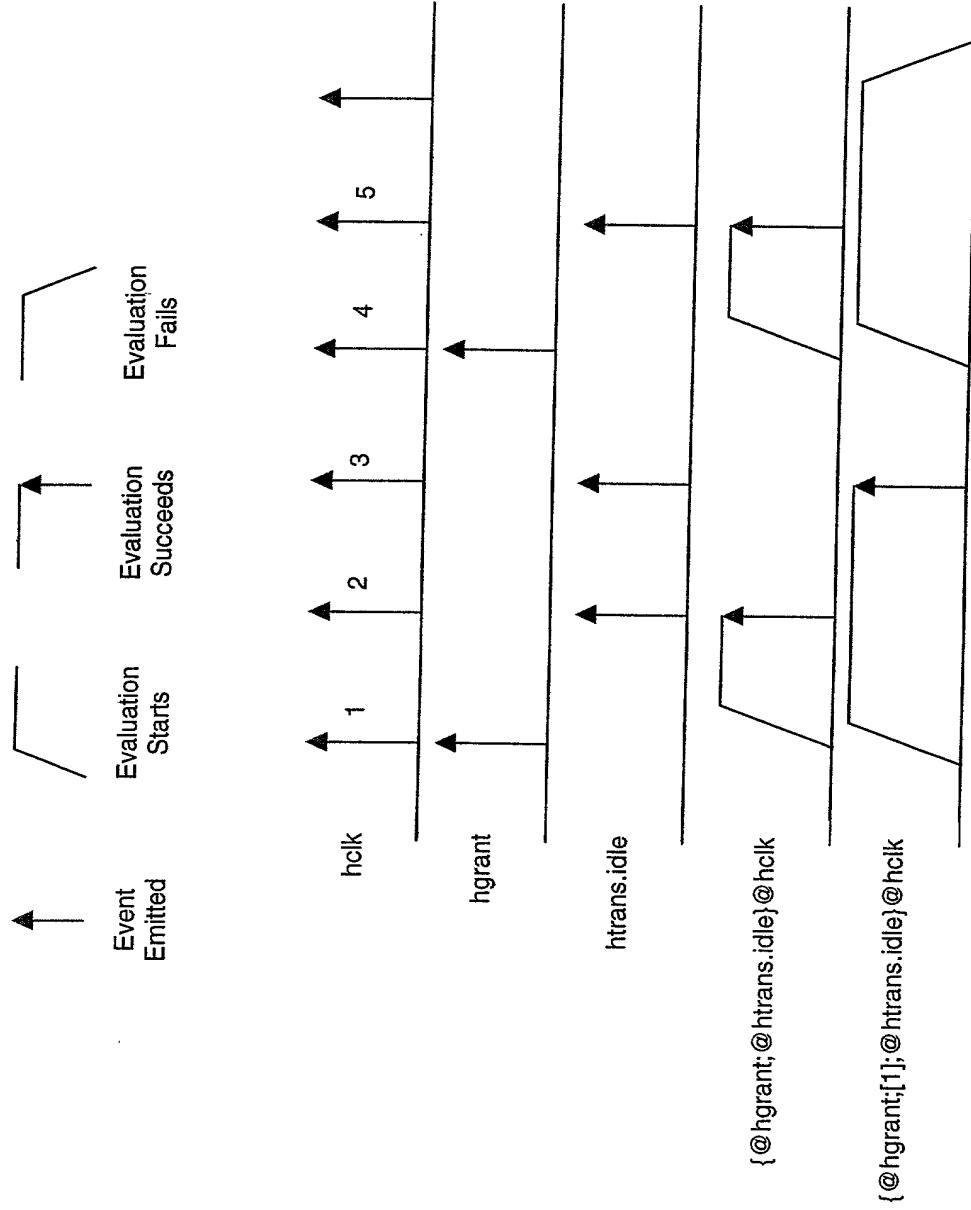


Fig. 8

9  
10

FIG. 9A

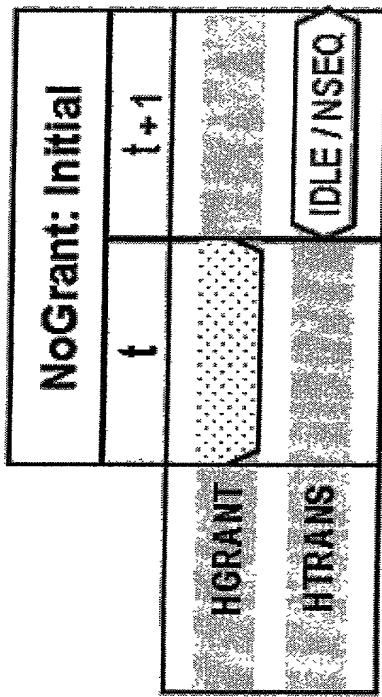
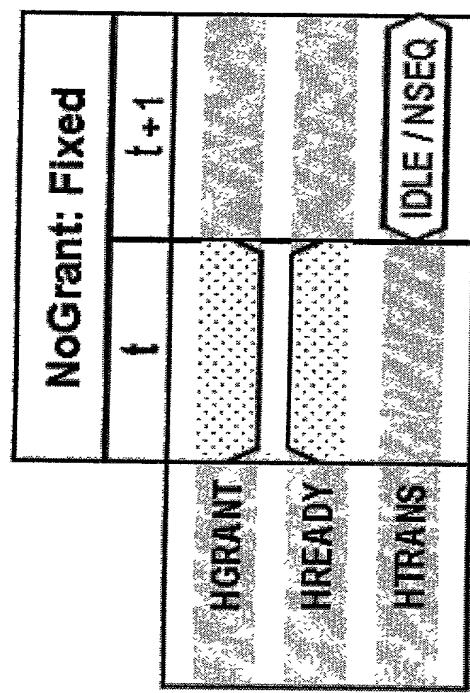


FIG. 9B



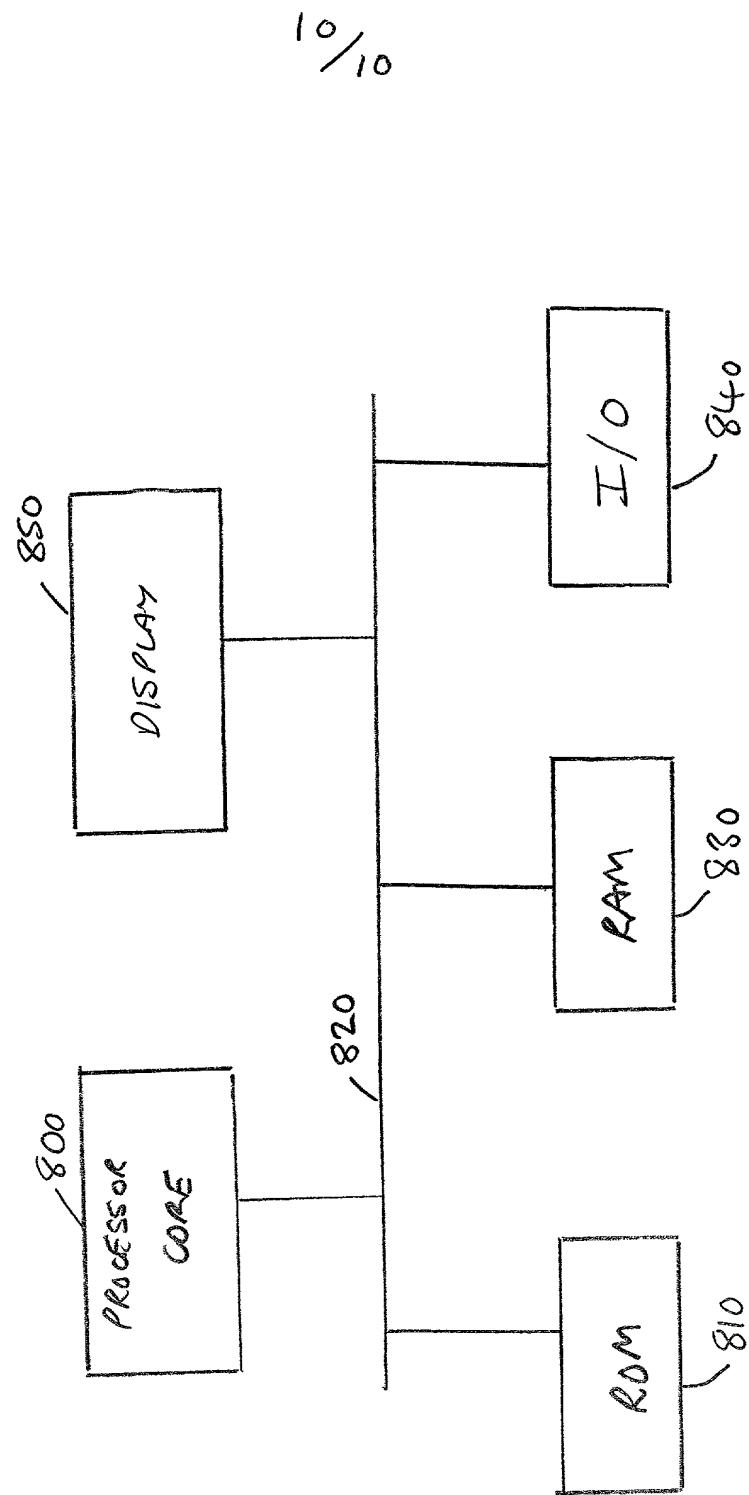


FIG. 10